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				Application Number	10/734708
				Filing Date	12-11-03
				First Named Inventor	Visokay et al.
				Group Art Unit	2813
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-35227

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
TP	AA	5,352,623		Kamiyama	10/04/1994	Entire Document
	AB	5,891,809		Chau et al.	04/06/1999	Entire Document
	AC	6,013,553		Wallace et al.	01/11/2000	Entire Document
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FOREIGN PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)		
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TS	CA	"Fabrication of HfSiON Gate Dielectrics by Plasma Oxidation and Nitridation, Optimized for 65nm node Low Power CMOS Applications", SEIJI INUMIYA, KATSUYUKI SEKINE, SHOKO NIWA, AKIO KANEKO, MOTOYUKI SATO, TAKESHI WATANABE, HIRONOBU FUKUI, YOSHIKI KAMATA, MASATO KOYAMA, AKIRA NISHIYAMA, MARIKO TAKAYANAGI, KAZUHIRO EGUCHI and YOSHITAKA TSUNASHIMA, 2003 Symposium on VLSI Technology Digest of Technical Papers, 2 pgs.	
	CB		

Examiner Signature		Date Considered	10/14/05
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